

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	Flash Drive/Reader with Serial-Port Controller and Flash-Memory Controller Mastering a Second RAM-Buffer Bus Parallel to a CPU Bus							
Application Number :								
Date :								
First Named Applicant:	Horng-Yee Chou							
Confirmation Number:								
Attorney Docket Number:	ML-18							
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Stuart T Auvinen Registered Number: 36435</td><td>/e/stuauvinen</td><td>Agent</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Stuart T Auvinen Registered Number: 36435	/e/stuauvinen	Agent
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Documents being submitted:	Files
us-ids	ML18ex-usidst.xml us-ids.dtd us-ids.xsl
us-request	ML18ex-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	ML18ex-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	ML18e-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd fgML18e1.TIF fgML18e2.TIF fgML18e3.TIF fgML18e4.TIF fgML18e5.TIF fgML18e6.TIF fgML18e7.TIF fgML18e8.TIF fgML18e9.TIF OathML18e.tif
us-declaration	
<b>Comments</b>	